

Atty Docket No. NSC1-M1700**DECLARATION FOR PATENT APPLICATION**

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

**DMOS TRANSISTOR STRUCTURE WITH GATE ELECTRODE TRENCH HIGH DENSITY INTEGRATION AND METHOD OF FABRICATING THE STRUCTURE**

the specification of which (check one) ☒ is attached hereto or ☐ was filed on \_\_\_\_\_ as Application No. \_\_\_\_\_ and was amended on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose all information which is material to patentability as defined in 37 CFR § 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)			<u>Priority Claimed</u>	
			<u>Yes</u>	<u>No</u>
_____	_____	_____	<input type="checkbox"/>	<input type="checkbox"/>
Number	Country	Day/Month/Year Filed		
_____	_____	_____	<input type="checkbox"/>	<input type="checkbox"/>
Number	Country	Day/Month/Year Filed		

I hereby claim the benefit under 35 U.S.C. § 119(3) of any United States provisional application(s) below:

_____	_____
Application Number	Filing Date
_____	_____
Application Number	Filing Date
_____	_____
Application Number	Filing Date

I hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s), or § 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose all information which is material to patentability as defined in 37 CFR § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

Application Number	Filing Date	Status: Patented, Pending, Abandoned
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Application Number	Filing Date	Status: Patented, Pending, Abandoned
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Application Number	Filing Date	Status: Patented, Pending, Abandoned
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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under 18 U.S.C. § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole or first inventor Waclaw C. Koscielniak

Inventor's signature

W. C. Koscielniak

7/31/02  
Date

Residence 444 Saratoga Ave. #12A, Santa Clara, CA 95050

Citizenship Poland

Post Office Address 444 Saratoga Ave. #12A, Santa Clara, CA 95050

Full name of second joint inventor, if any, \_\_\_\_\_

Inventor's signature \_\_\_\_\_

\_\_\_\_\_  
Date

Residence \_\_\_\_\_

Citizenship \_\_\_\_\_

Post Office Address \_\_\_\_\_

Full name of third joint inventor, if any, \_\_\_\_\_

Inventor's signature \_\_\_\_\_

\_\_\_\_\_  
Date

Residence \_\_\_\_\_

Citizenship \_\_\_\_\_

Post Office Address \_\_\_\_\_

Full name of fourth joint inventor, if any, \_\_\_\_\_

Inventor's signature \_\_\_\_\_

\_\_\_\_\_  
Date

Residence \_\_\_\_\_

Citizenship \_\_\_\_\_

Post Office Address \_\_\_\_\_

Full name of fifth joint inventor, if any, \_\_\_\_\_

Inventor's signature \_\_\_\_\_

\_\_\_\_\_  
Date

Residence \_\_\_\_\_

Citizenship \_\_\_\_\_

Post Office Address \_\_\_\_\_

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Waclaw C. Koscielniak

Appln. No.: 10/212,392

Filed: August 5, 2002

For: DMOS TRANSISTOR STRUCTURE WITH  
GATE ELECTRODE TRENCH FOR HIGH  
DENSITY INTEGRATION AND METHOD  
OF FABRICATING THE STRUCTURE

Group Art: 2815

Examiner: Unknown

**POWER OF ATTORNEY BY  
ASSIGNEE**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

NATIONAL SEMICONDUCTOR CORPORATION, assignee of the entire right title and interest in the above-identified application by assignment dated July 31, 2002, which assignment was recorded in the Patent and Trademark Office on August 6, 2002, at Reel 13170, Frame 178, hereby appoints the members of the firm of STALLMAN & POLLOCK LLP, a firm composed of:

Michael A. Stallman	29,444	Kathleen A. Frost	37,326	Stephen M. Everett	30,050
Michael J. Pollock	29,098	Brian J. Keating	39,520		

121 Spear Street, Suite 290, San Francisco, CA 94105, (415) 512-1312, FAX (415) 512-1362, and the following members of the National Semiconductor Corporation Intellectual Property Department:

Christopher J. Byrne	32,204	Andrew S. Viger	28,552	Coleman F. Reif	38,593
John Maxin	34,668	Eugene C. Conser	39,149	Allen R. Tremain	40,207
Pater Y. Wang	40,452				

as its attorneys with full power of substitution to prosecute this application and to transact all business in the Patent and Trademark Office in connection therewith.

Please direct all correspondence regarding this application to the following:

STALLMAN & POLLOCK LLP  
Attn: Michael J. Pollock  
121 Spear Street, Suite 290  
San Francisco, CA 94105

Telephone: (415) 512-1312  
Facsimile: (415) 512-1362

Dated: 11/4/02

NATIONAL SEMICONDUCTOR CORPORATION

By: 

Name: Christopher J. Byrne

Title: Director of Intellectual Property and Technology Licensing



UNITED STATES  
PATENT AND  
TRADEMARK OFFICE

NSC1-M1700

OCTOBER 15, 2002

STALLMAN & POLLOCK LLP  
MICHAEL J. POLLOCK  
121 SPEAR STREET  
SUITE 290  
SAN FRANCISCO, CA 94105

PTAS

Under Secretary of Commerce For Intellectual Property and  
Director of the United States Patent and Trademark Office  
Washington, DC 20231  
www.uspto.gov

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OCT 21 2002

STALLMAN & POLLOCK LLP



\*102187345A\*

UNITED STATES PATENT AND TRADEMARK OFFICE  
NOTICE OF RECORDATION OF ASSIGNMENT DOCUMENT

THE ENCLOSED DOCUMENT HAS BEEN RECORDED BY THE ASSIGNMENT DIVISION OF THE U.S. PATENT AND TRADEMARK OFFICE. A COMPLETE MICROFILM COPY IS AVAILABLE AT THE ASSIGNMENT SEARCH ROOM ON THE REEL AND FRAME NUMBER REFERENCED BELOW.

PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. THE INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. IF YOU SHOULD FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 703-308-9723. PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE, ASSIGNMENT DIVISION, BOX ASSIGNMENTS, CG-4, 1213 JEFFERSON DAVIS HWY, SUITE 320, WASHINGTON, D.C. 20231.

RECORDATION DATE: 08/06/2002

REEL/FRAME: 013170/0178  
NUMBER OF PAGES: 2

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:

KOSCIELNIAK, WACLAW C.

DOC DATE: 07/31/2002

ASSIGNEE:

NATIONAL SEMICONDUCTOR CORPORATION  
2900 SEMICONDUCTOR DRIVE  
SANTA CLARA, CALIFORNIA 95051-8090

✓ SERIAL NUMBER: 10212392  
PATENT NUMBER:

FILING DATE: 08/05/2002  
ISSUE DATE:

SHARON BROOKS, EXAMINER  
ASSIGNMENT DIVISION  
OFFICE OF PUBLIC RECORDS

08-13-2002

Pat No.: NSC1-M1700

U.S. DEPARTMENT OF COMMERCE

Patent and Trademark Office



Tab settings

102187345

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

Waclaw C. Koscielniak

08/06/02

Additional names(s) of conveying party(ies)

☐ Yes ☐ No

3. Nature of conveyance:

☒ Assignment

☐ Merger

☐ Security Agreement

☐ Change of Name

☐ Other

Execution Date: July 31, 2002

2. Name and address of receiving party(ies):

Name: National Semiconductor Corporation

Address: 2900 Semiconductor Drive

City: Santa Clara

State/Prov.: CA

Country: U.S.A.

ZIP: 95051-8090

Additional name(s) & address(es)

☐ Yes ☐ No

4. Application number(s) or patent numbers(s):

10/212392

If this document is being filed together with a new application, the execution date of the application is: July 31, 2002

Patent Application No.

Filing date

B. Patent No.(s)

08/08/2002 WABDELRI 00000014 10212392

02 FC:581

40.00 OP

Additional numbers

☐ Yes ☐ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Michael J. Pollock

Registration No. 29,098

Address: Stallman & Pollock LLP

121 Spear Street, Suite 290

City: San Francisco

State/Prov.: CA

Country: U.S.A.

ZIP: 94105

6. Total number of applications and patents involved:

1

7. Total fee (37 CFR 3.41):.....\$ 40.00

☒ Enclosed - Any excess or insufficiency should be credited or debited to deposit account

☐ Authorized to be charged to deposit account

8. Deposit account number:

50-1703

(Attach duplicate copy of this page if paying by deposit account)

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Michael J. Pollock

Signature

8-5-2002

Name of Person Signing

Signature

2

Date

Total number of pages including cover sheet, attachments, and

**ASSIGNMENT**

WHEREAS, I, Waclaw C. Koscielniak, hereinafter referred to as "ASSIGNORS", have invented certain new and useful improvements as described and set forth in the below-identified application for United States Letters Patent:

Title of Invention: DMOS TRANSISTOR STRUCTURE WITH GATE ELECTRODE TRENCH FOR HIGH DENSITY INTEGRATION AND METHOD OF FABRICATING THE STRUCTURE

Application (Declaration/Oath) Execution Date: \_\_\_\_\_

Filing Date: \_\_\_\_\_ Application No.: \_\_\_\_\_

WHEREAS, NATIONAL SEMICONDUCTOR CORPORATION, a corporation of the State of Delaware, 2900 Semiconductor Drive, Santa Clara, California 95051-8090, hereinafter referred to as "ASSIGNEE", is desirous of acquiring the entire right, title and interest in the said invention and application and in any Letters Patent which may be granted on the same;

NOW, THEREFORE, TO ALL WHOM IT MAY CONCERN: Be it known that, for One Dollar (\$1.00) and other good and valuable consideration, receipt of which is hereby acknowledged by Assignors, Assignors have sold, assigned and transferred, and by these presents do sell, assign and transfer unto the said Assignee, and Assignee's successors and assigns, all right, title and interest in and to the said invention, said application for United States Letters Patent, and any Letters Patent which may hereafter be granted on the same in the United States and all countries throughout the world including any divisions, renewals, continuations in whole or in part, substitutions, conversions, reissues, prolongations or extensions thereof, said interest to be held and enjoyed by said Assignee as fully and exclusively as it would have been held and enjoyed by said Assignors had this assignment and transfer not been made, to the full end and term of any such Letters Patent.

Assignors further agree that they will, without charge to said Assignee, but at Assignee's expense, cooperate with Assignee in the prosecution of said application and/or applications; execute, verify, acknowledge and deliver all such further papers, including applications for Letters Patent and for the reissue thereof, and instruments of assignment and transfer thereof, and will perform such other acts as Assignee lawfully may request, to obtain or maintain Letters Patent for said invention and improvement in any and all countries, and to vest title thereto in said Assignee, or Assignee's successors and assigns.

IN TESTIMONY WHEREOF, Assignor has hereunto signed his/her name to this assignment on the date indicated below.

Date: 7/31/02

W. C. Koscielniak  
Waclaw C. Koscielniak

State of California

County of Santa Clara

On July 31, 2002 before me, Liza Smolyanitsky  
personally appeared Waclaw C. Koscielniak personally known to me or proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/~~she~~ they executed the same in his/~~her~~ their authorized capacity(~~ies~~); and that by his/~~her~~ their signature(~~s~~) on the instrument the person(~~s~~) or the entity upon behalf of which the person(~~s~~) acted, executed the instrument.

WITNESS my hand and official seal.

Liza Smolyanitsky  
Signature of Notary

SEAL

